MUX3Select

Add 6

Add

6

Add

6

Add

6

3

MUX

MUX

MUX

Ans 3

NotEqual

Count2

EqTarget2

2

Counter to 36

clk

60Hz Clock

LoadVal

LoadRow

LoadCol

MUX1Select

MUX2Select

2

1

WriteEn

EqTarget1

Count1

Count3

UARTdone

UARTsend

3

1

Data to Send

DataOut

ST

DataOut

DataOut

LEDs

6

To Serial

TxBit

Ans 3

3

BinaryOut

ST

BinaryOut

ValAddSelect

ValAddSelect

Incorrect

6

6

3

3

Value

3

3

5

Value 3

3

3

3

3

BooardSW

3

PmodSW

3

Compare

Col

3

Row

3

Col

3

Row

3

Value

3

Counter to 36

UART

+48

RAM

Solution ROM

Counter to 21

Start ROM

2D

To

1D

7-Seg

Control

Value Register

Row Register

Col Register